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CAI et al.(10) Pub. No.: **US 2001/0049770 A1**(43) Pub. Date: **Dec. 6, 2001**(54) **BUFFER MEMORY MANAGEMENT IN A
SYSTEM HAVING MULTIPLE EXECUTION
ENTITIES**(21) Appl. No.: **09/207,278**(22) Filed: **Dec. 8, 1998**(76) Inventors: **ZHONG-NING CAI, PORTLAND, OR
(US); TOSAKU NAKANISHI,
SARATOGA, CA (US)****Publication Classification**(51) Int. Cl.⁷ **G06F 12/08**(52) U.S. Cl. **711/129; 711/133; 711/128;
711/122**

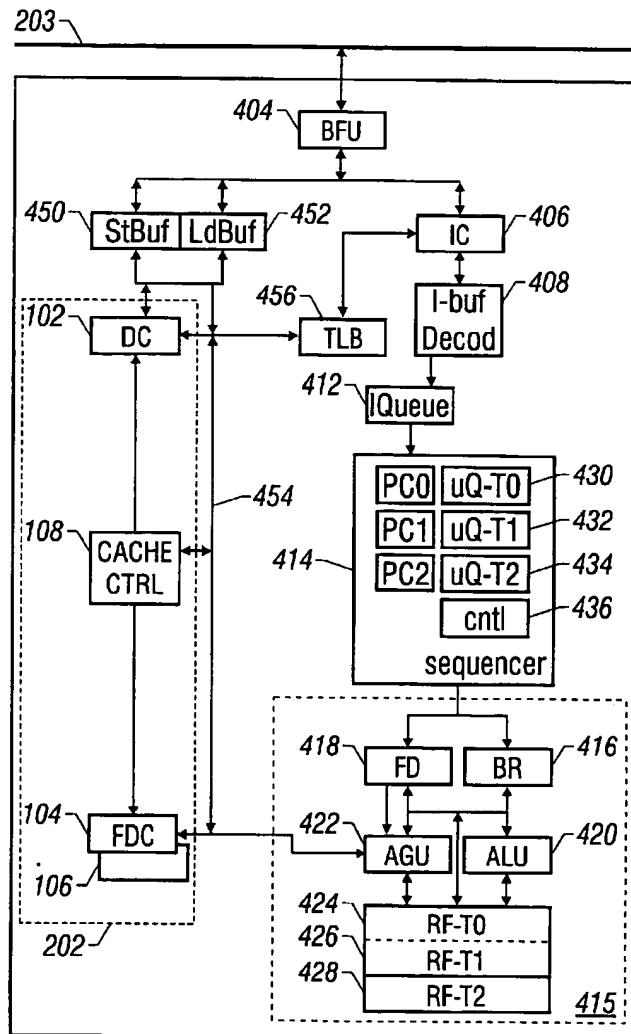
Correspondence Address:

**TIMOTHY N TROP
TROP PRUNER HU & MILES
8554 KATY FREEWAY
SUITE 100
HOUSTON, TX 77024**

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(57) **ABSTRACT**

A system includes multiple program execution entities (e.g., tasks, processes, threads, and the like) and a cache memory having multiple sections. An identifier is assigned to each execution entity. An instruction of one of the execution entities is retrieved and an associated identifier is decoded. Information associated with the instruction is stored in one of the cache sections based on the identifier.



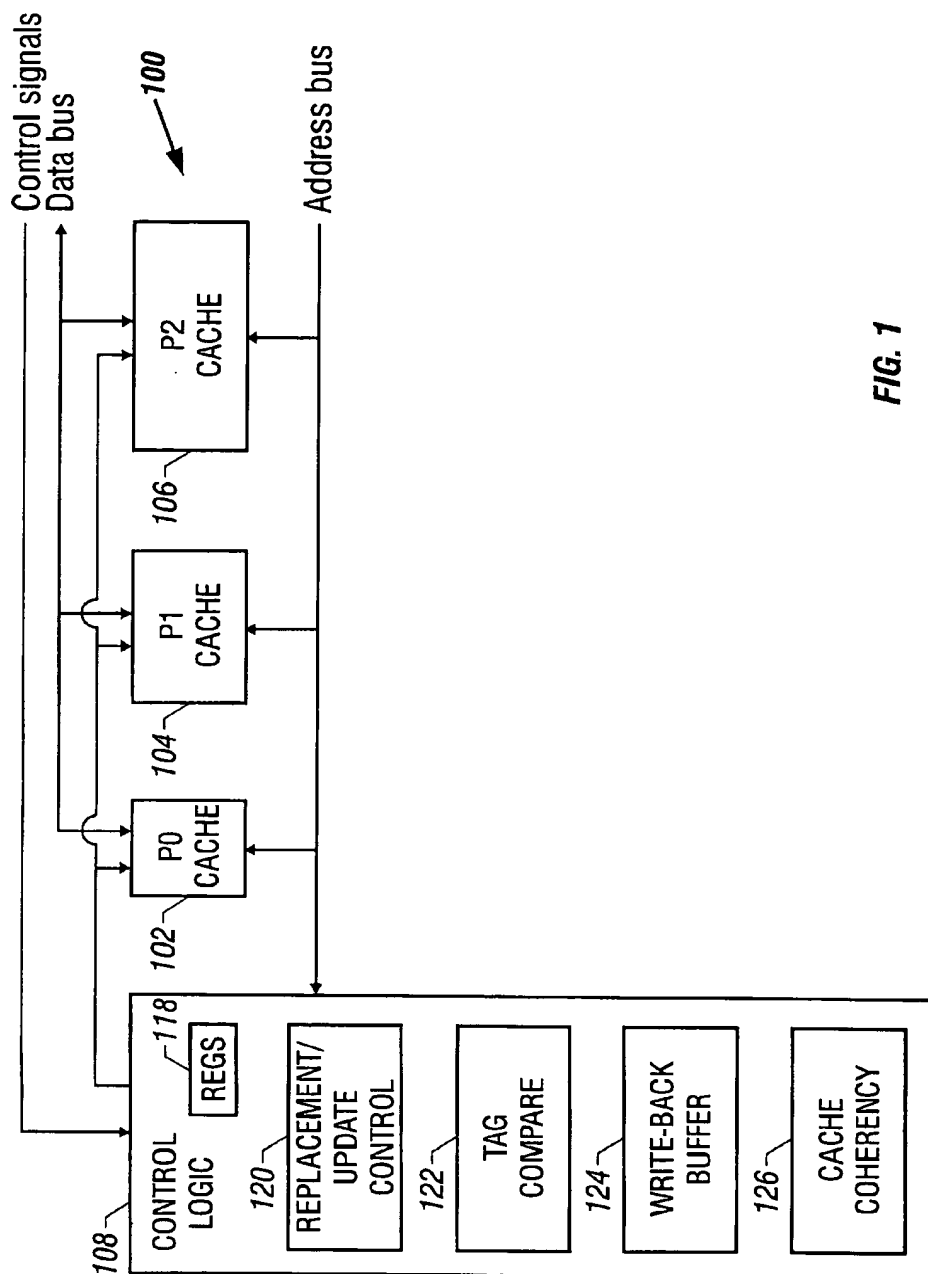


FIG. 1

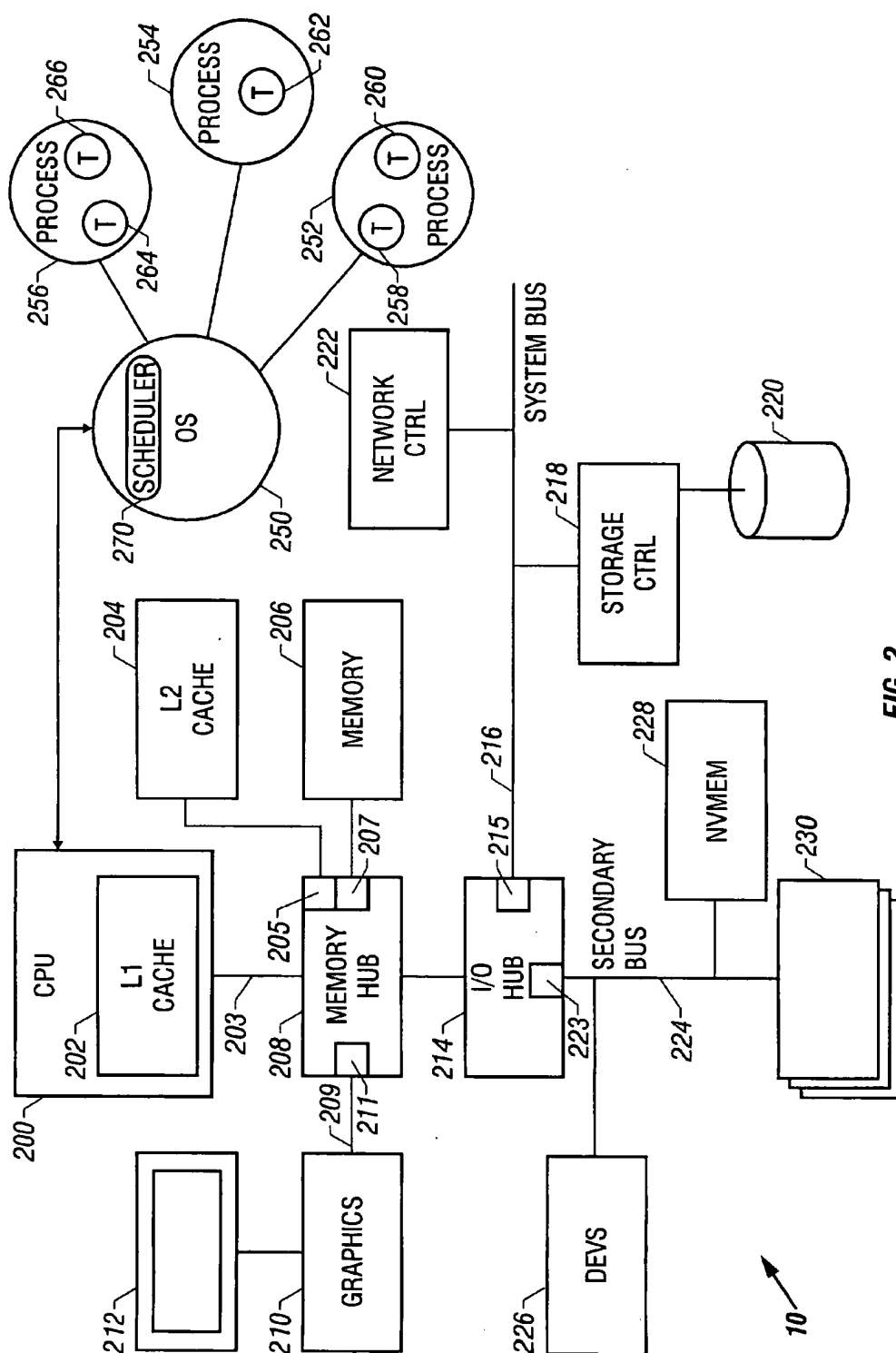


FIG. 2

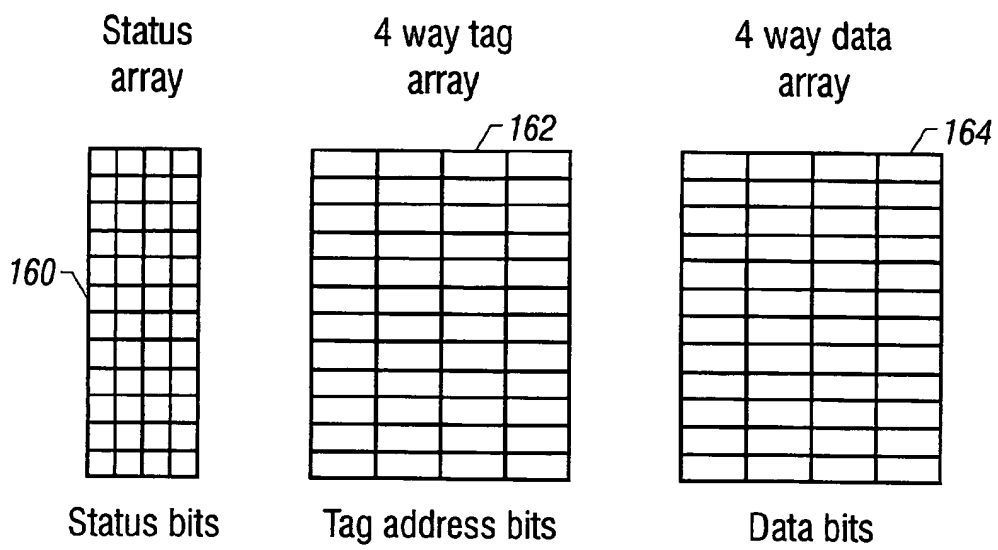
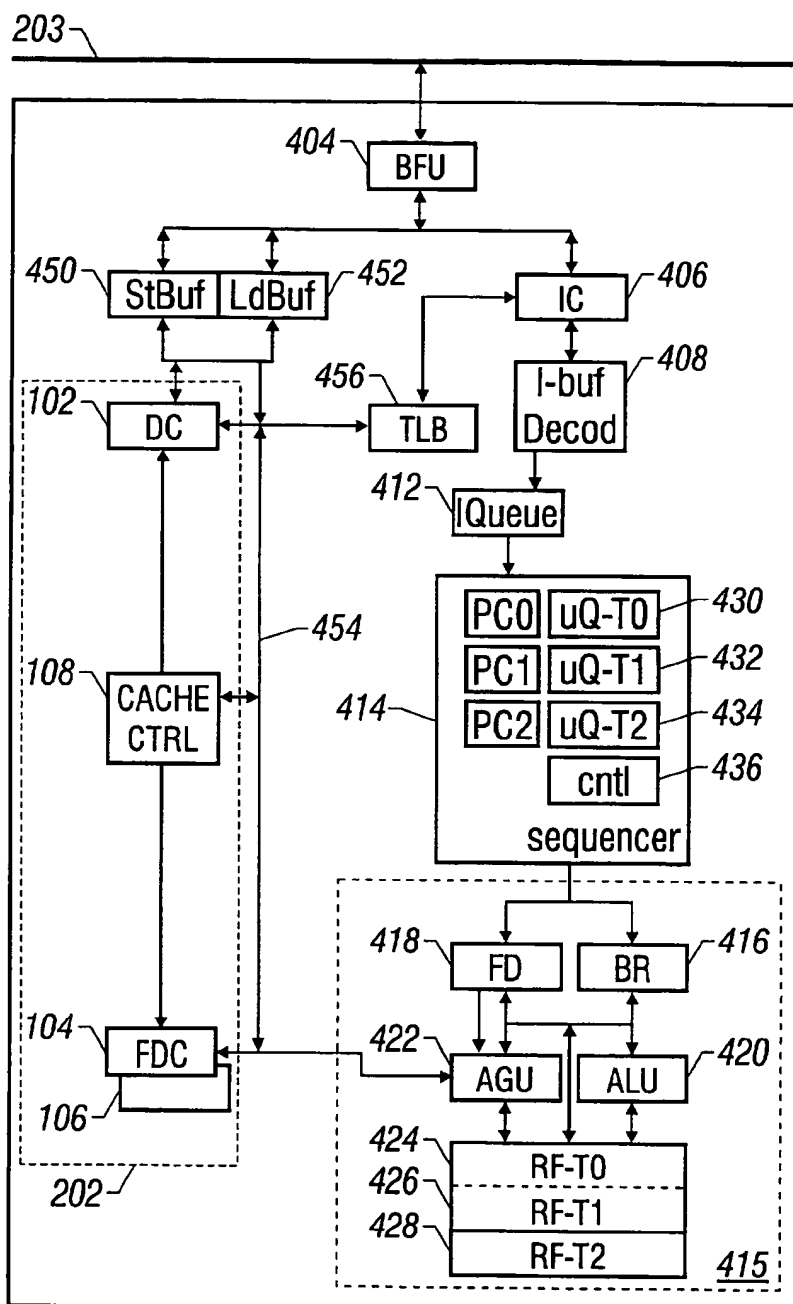


FIG. 3



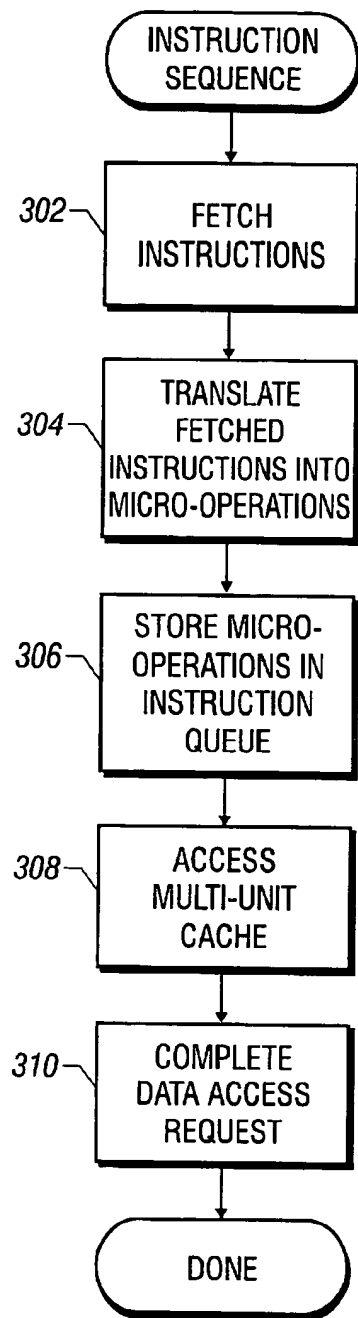


FIG. 5

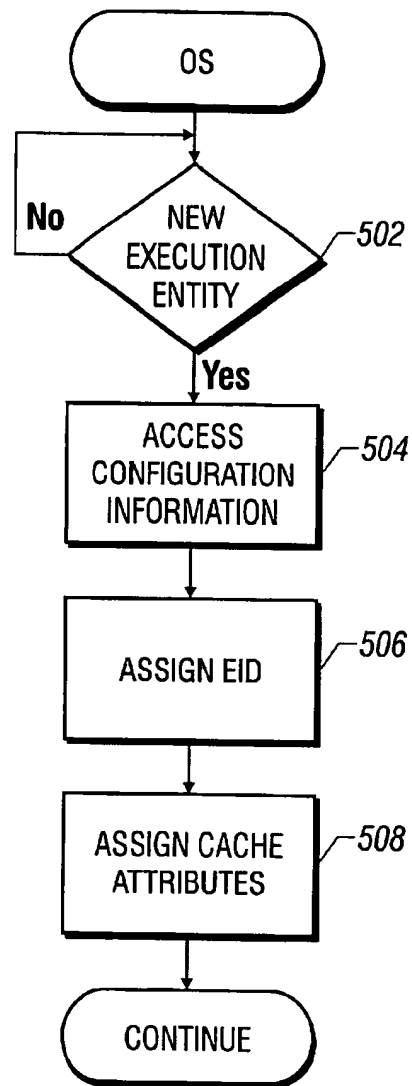


FIG. 6